



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


APPLICANT: John D. Hyde et al.  
SERIAL NO.: 10/661,037  
FILING DATE: September 12, 2003  
TITLE: pFET Synapse Transistor With Structure For Facilitating Charge Injection  
and/or Tunneling With Respect to a Floating Gate  
EXAMINER: Ida M. Soward  
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ART UNIT: 2822

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**CERTIFICATE OF MAILING**

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date printed below:

Date: June 8, 2006

  
Laura L. Hulac

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Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT AND/OR RESPONSE TO OFFICE ACTION**

Sir:

In response to the Office Action mailed **December 8, 2005**, please amend the subject application as indicated.

**Amendments to the Claims**, if any, are reflected in the Listing of Claims beginning on page 2.

**Remarks** begin on page 10.